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|-----------------------------------|---------------------------------------|---|-------------|
| <b>Notice of References Cited</b> | Application/Control No.<br>09/813,765 | Applicant(s)/Patent Under<br>Reexamination<br>RUMBLE, ROBERT P. |             |
|                                   | Examiner<br>David Y. Jung             | Art Unit<br>2134  | Page 1 of 1 |

**U.S. PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Name | Classification |
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|   | N |  |                 |         |      |                |
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|   | R |  |                 |         |      |                |
|   | S |  |                 |         |      |                |
|   | T |  |                 |         |      |                |

**NON-PATENT DOCUMENTS**

| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)  |
|---|---|--|
|   | U | A high-speed processing LSI for RSA cryptograms using an improved adder circuit<br>Niimura, M.; Fuwa, Y.;<br>TENCON 2004. 2004 IEEE Region 10 Conference<br>Volume B, 21-24 Nov. 2004 Page(s):175 - 178 Vol. 2 |
|   | V | p-adic attack of knapsack cryptosystem<br>Abramov, A.; Modern Communication Technologies, 2001. SIBCOM-2001. The IEEE-Siberian Workshop of Students and Young Researchers<br>28-29 Nov. 2001 Page(s):3 - 13    |
|   | W | A novel unified architecture for public-key cryptography<br>Cilardo, A.; Mazzeo, A.; Mazzocca, N.; Romano, L.;<br>Design, Automation and Test in Europe, 2005. Proceedings<br>2005 Page(s):52 - 57 Vol. 3      |
|   | X |  |

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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